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Introspect Technology & Trust-Tek Corporation

**Practical Experiences in MIPI D-PHY<sup>SM</sup>  
and C-PHY<sup>SM</sup> Receiver Testing**

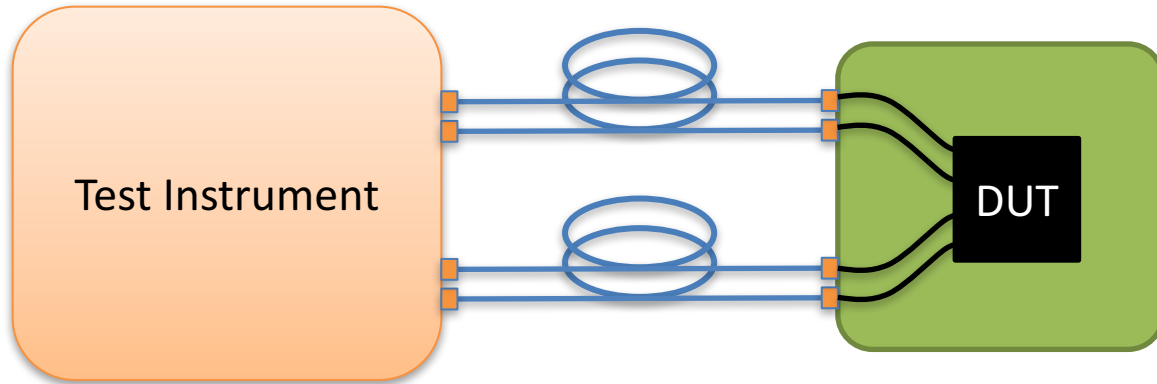
**HSINCHU CITY, TAIWAN**

**[MIPI.ORG/DEVCON](http://MIPI.ORG/DEVCON)**

**2017**

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# In Theory...



# In Real Life...

## Test Requirements Through Product Lifecycle Stages

### IP Bring-Up

- Test IP in isolation
- Theoretical case (perhaps!)

### Component Bring-Up

- Focus on entire chip functionality including interface
- Other effects start to appear

### Application Module Bring-Up

- Where is the receiver?
- Where is the channel?
- What stimulus to use?

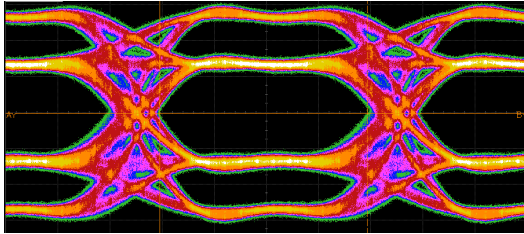
# Agenda

- Overview of receiver test
- Illustration of practical module implementations and evaluation platforms
- Recommendations and best practices



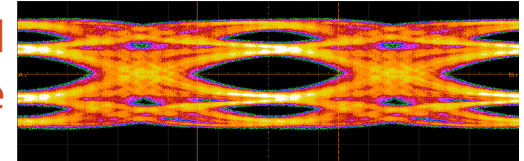
# Physics of Signal Transmission

Transmitter

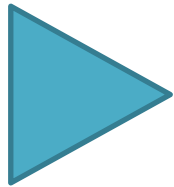


Large Signal  
Small Noise

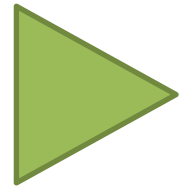
Receiver



Small Signal  
Large Noise



Channel (cable, connector, flex PCB, package)



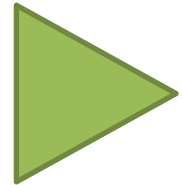
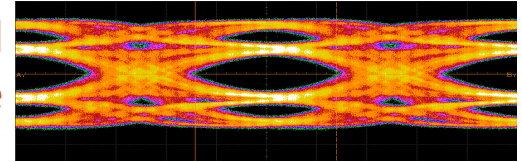
# Objective of Receiver Test

Generate a stressed signal as if it went through a channel

- Model ISI
- Model DCD
- Model Jitter

Receiver

Small Signal  
Large Noise



# MIPI D-PHY<sup>SM</sup> Receiver Specification

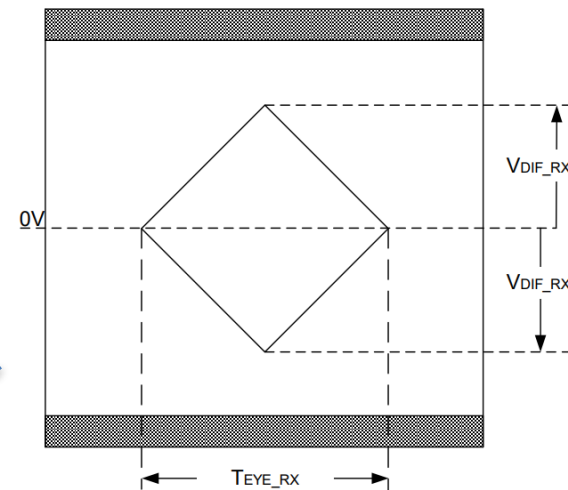
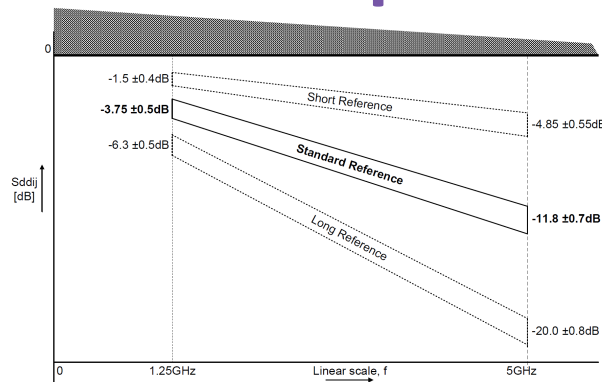
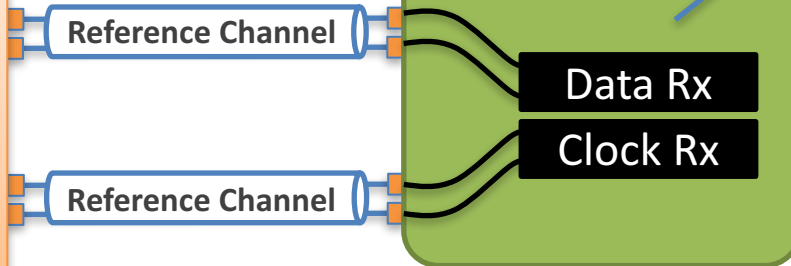


Figure 67 Receiver Eye Diagram Specification

MIPI D-PHY<sup>SM</sup>  
Generator



# MIPI C-PHY<sup>SM</sup> Receiver Specification

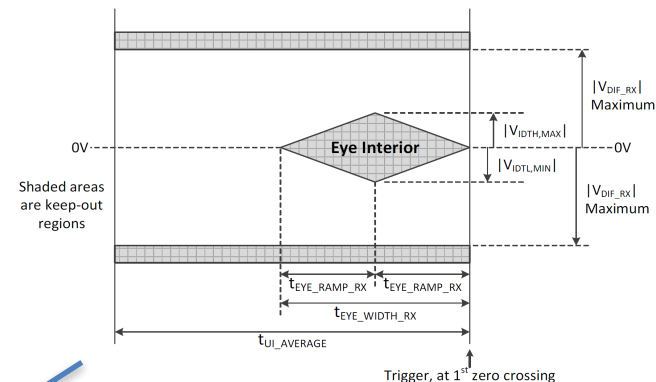
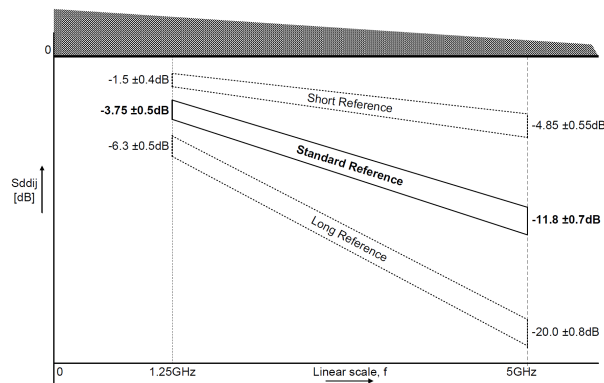
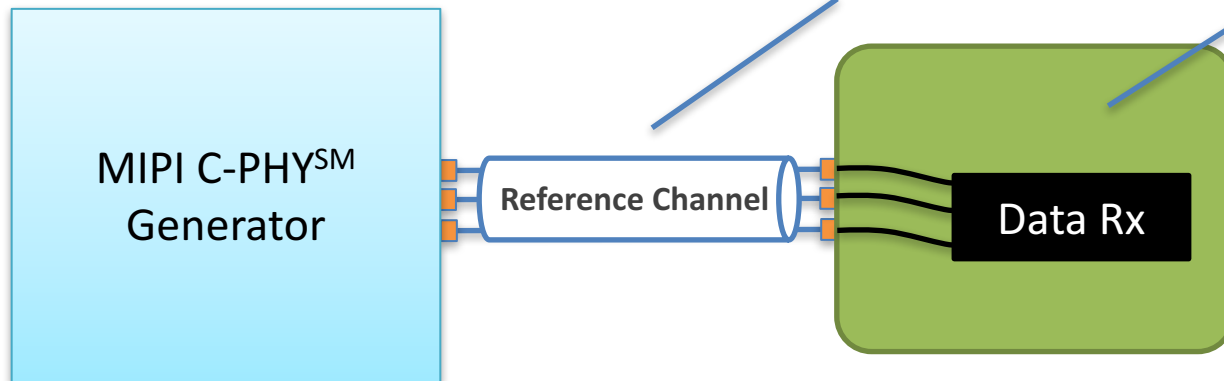
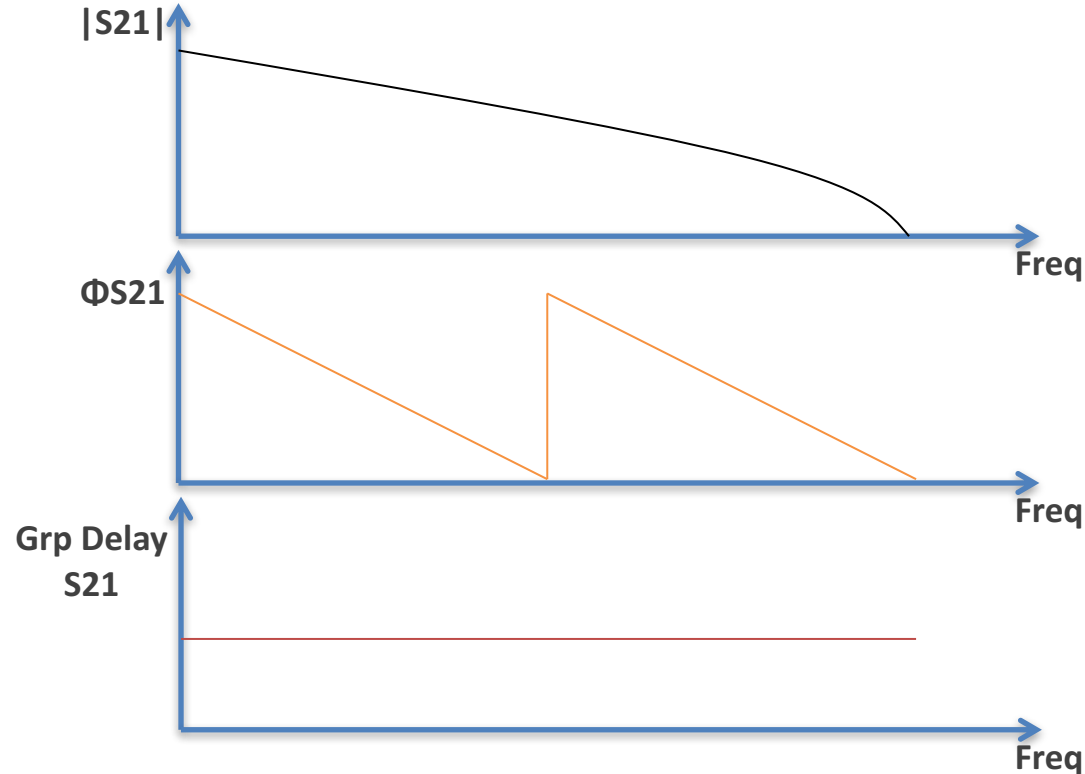


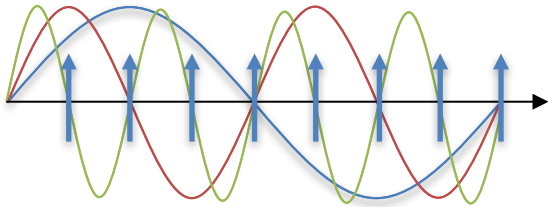
Figure 81 C-PHY Receiver Eye Diagram



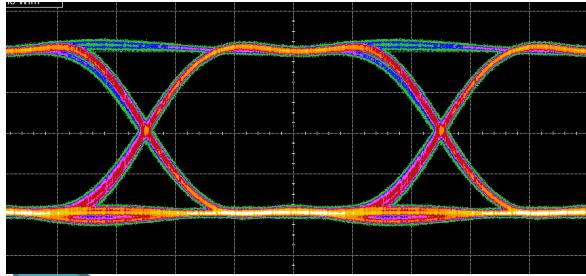
# The Linear Channel Model



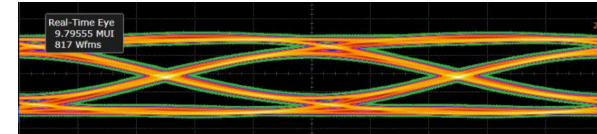
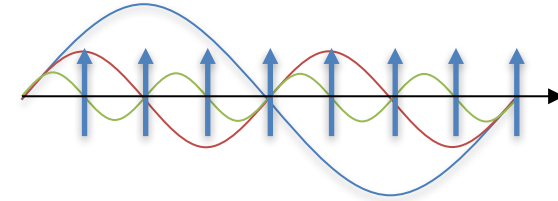
# The Linear Channel Model



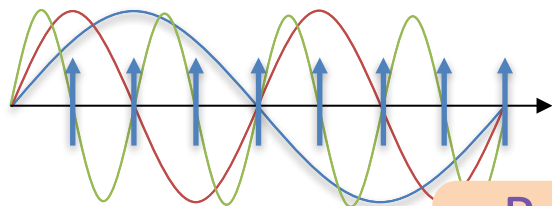
Timing Edges Largely Unaffected  
in a Linear Channel



Linear Channel

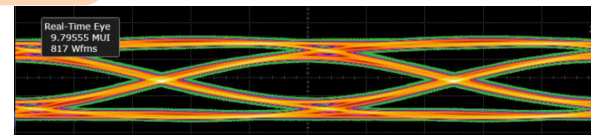
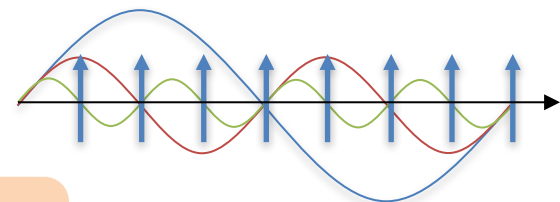
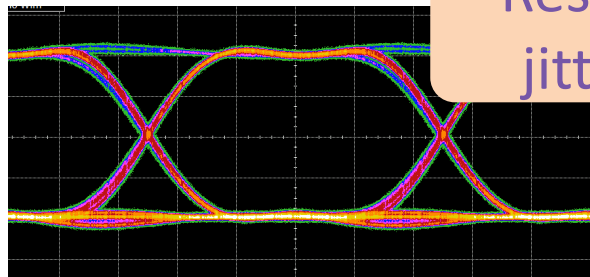


# The Linear Channel Model



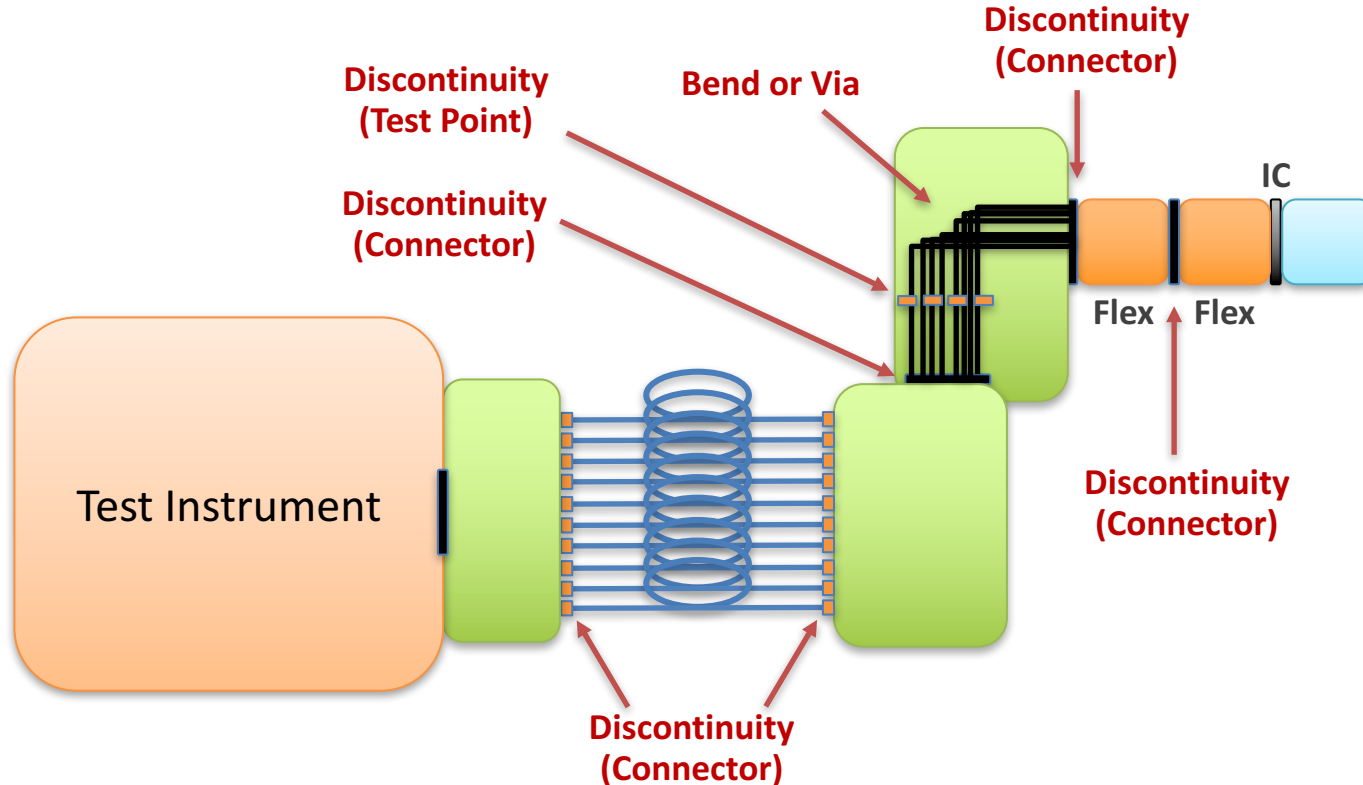
Timing Edges Largely Unaffected  
in a Linear Channel

Results in almost non-realistic  
jitter injection requirements



Linear Channel

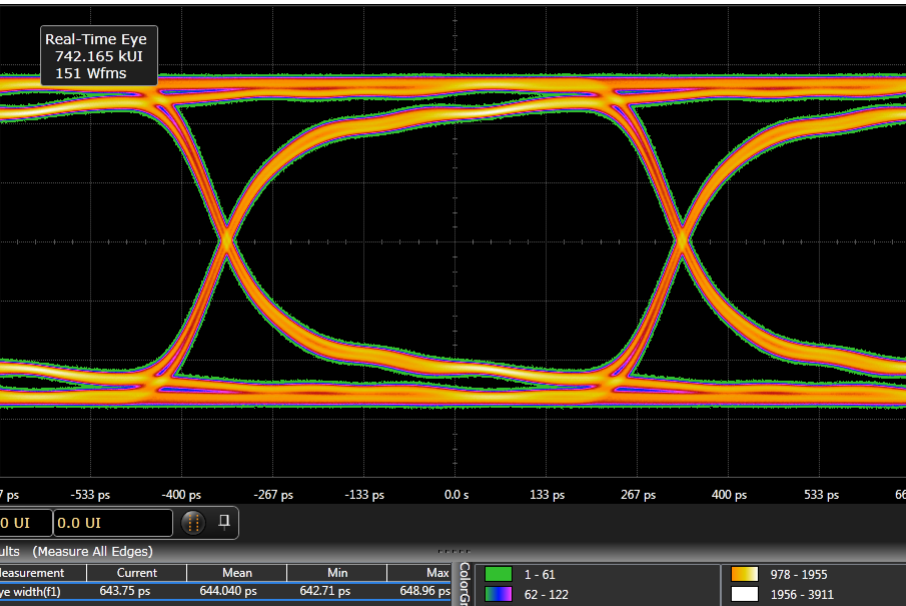
# Practical Evaluation Board... Nonlinear!



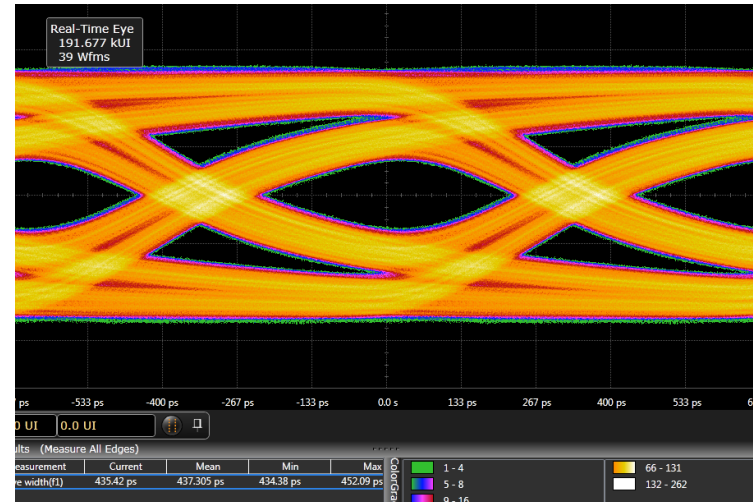


# Linear Channel Versus Non-Linear Channel

Tx Eye Diagram

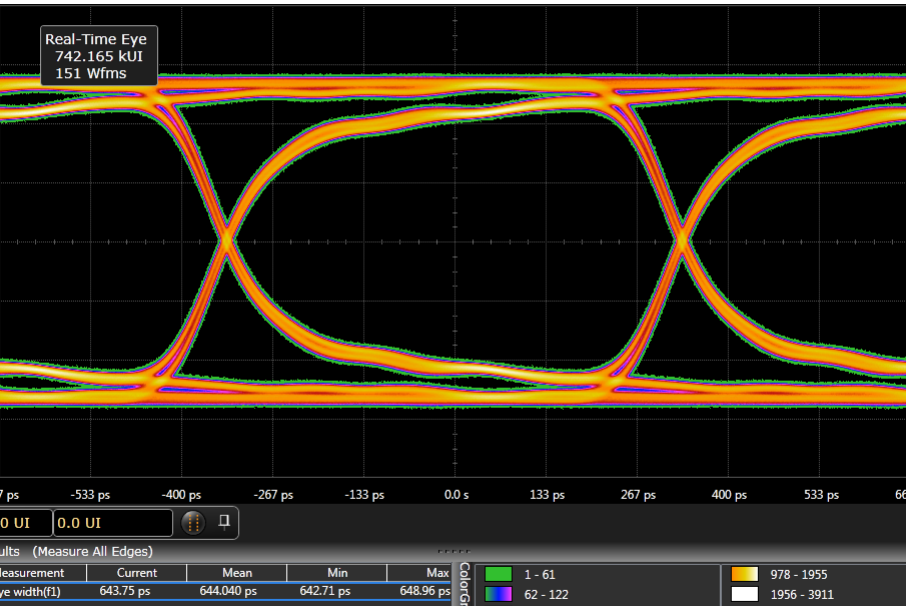


Linear Channel Eye Diagram (~0.3 UI Closure)

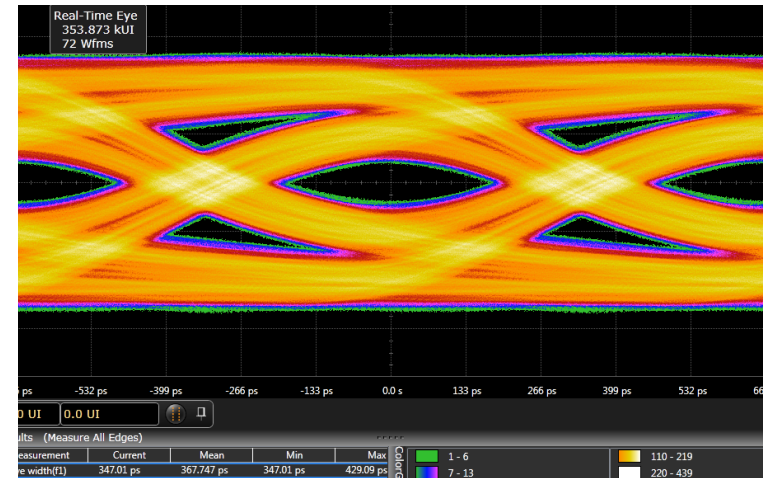


# Linear Channel Versus Non-Linear Channel

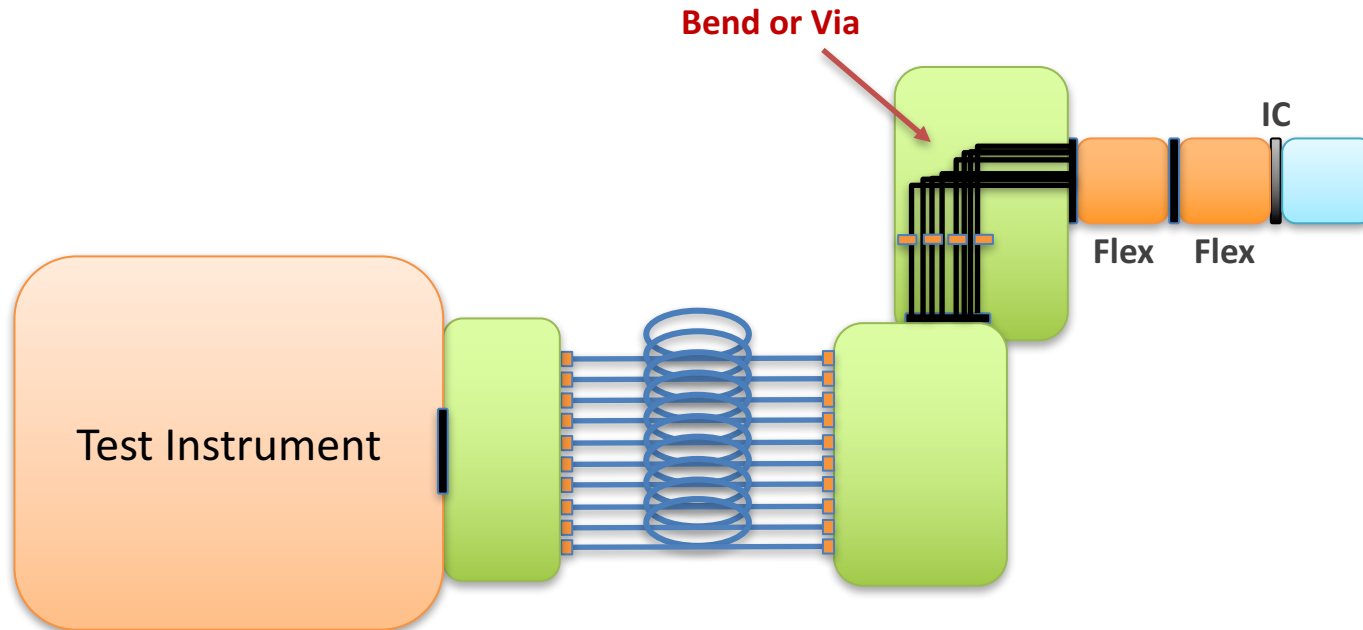
Tx Eye Diagram



Non-Linear Channel Eye Diagram (~0.4 UI Closure)  
Channel is 25% Shorter



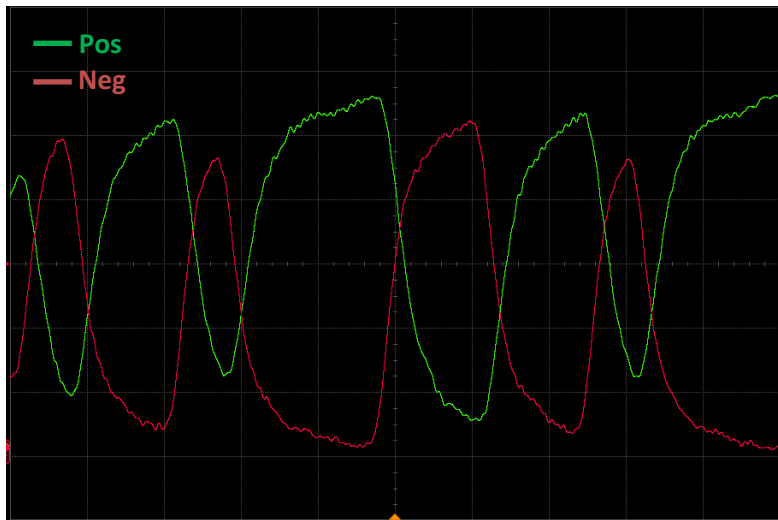
# Practical Experiences: Skew



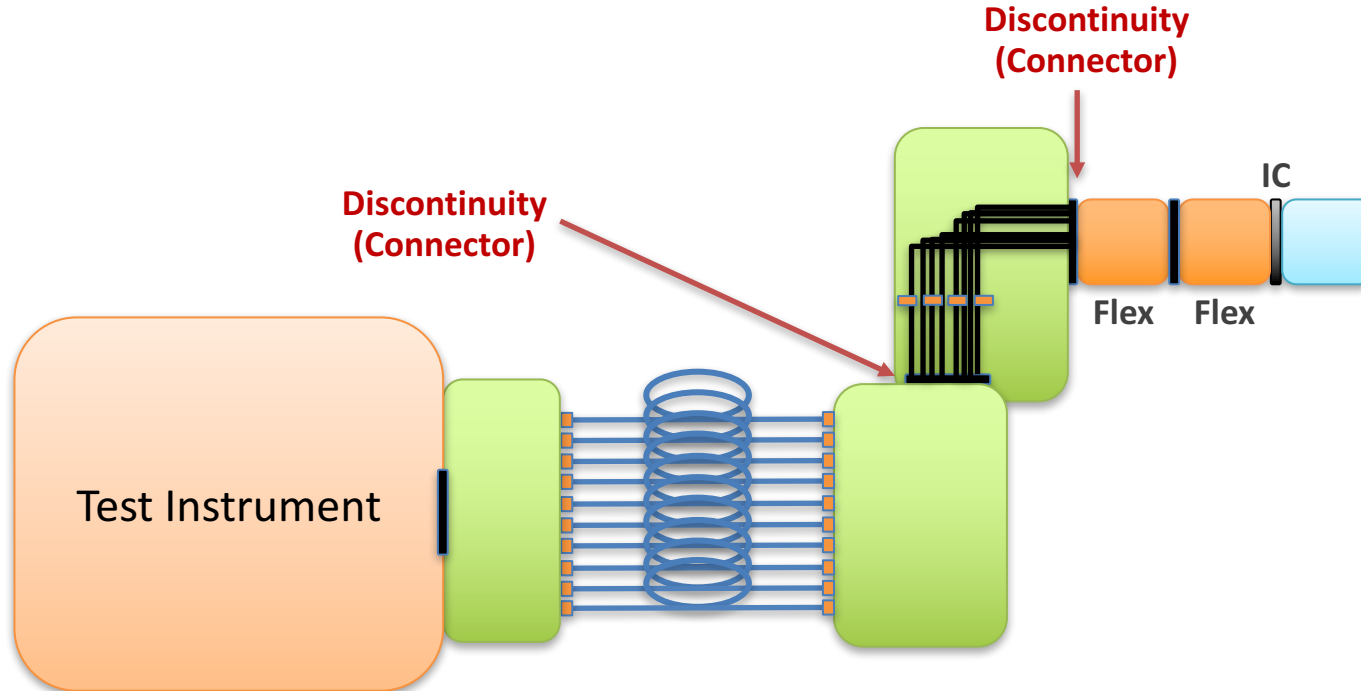
# Practical Experiences: Skew

Skew introduces common-mode noise (not good for receiver!)

**Difficult** to detect on differential scope eye diagram



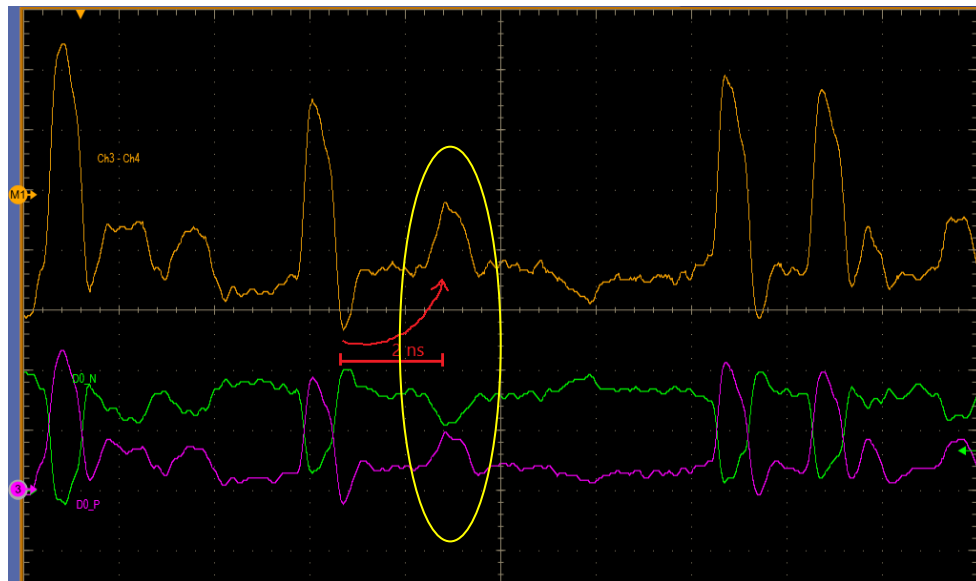
# Practical Experiences: Reflections



# Practical Experiences: Reflections

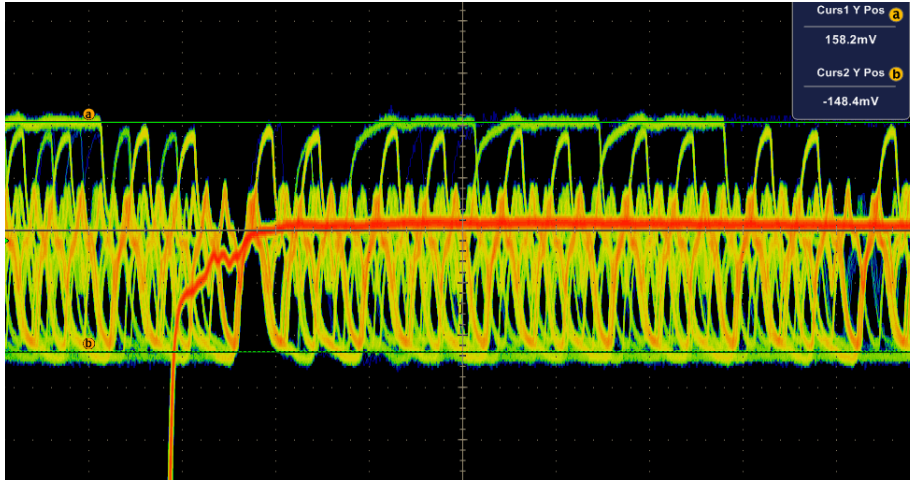
Worst-case example of a channel non-linearity

Signal rise time is sharp,  
but reflections can cause  
bit flips

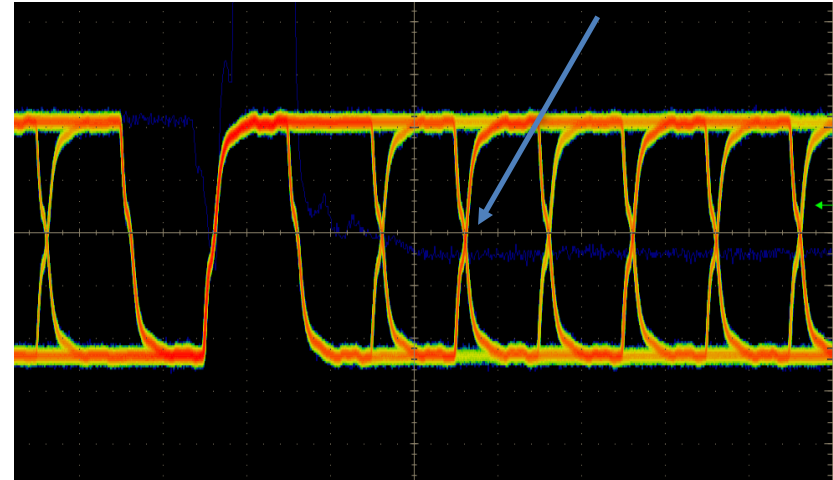


## Practical Experiences: Reflections

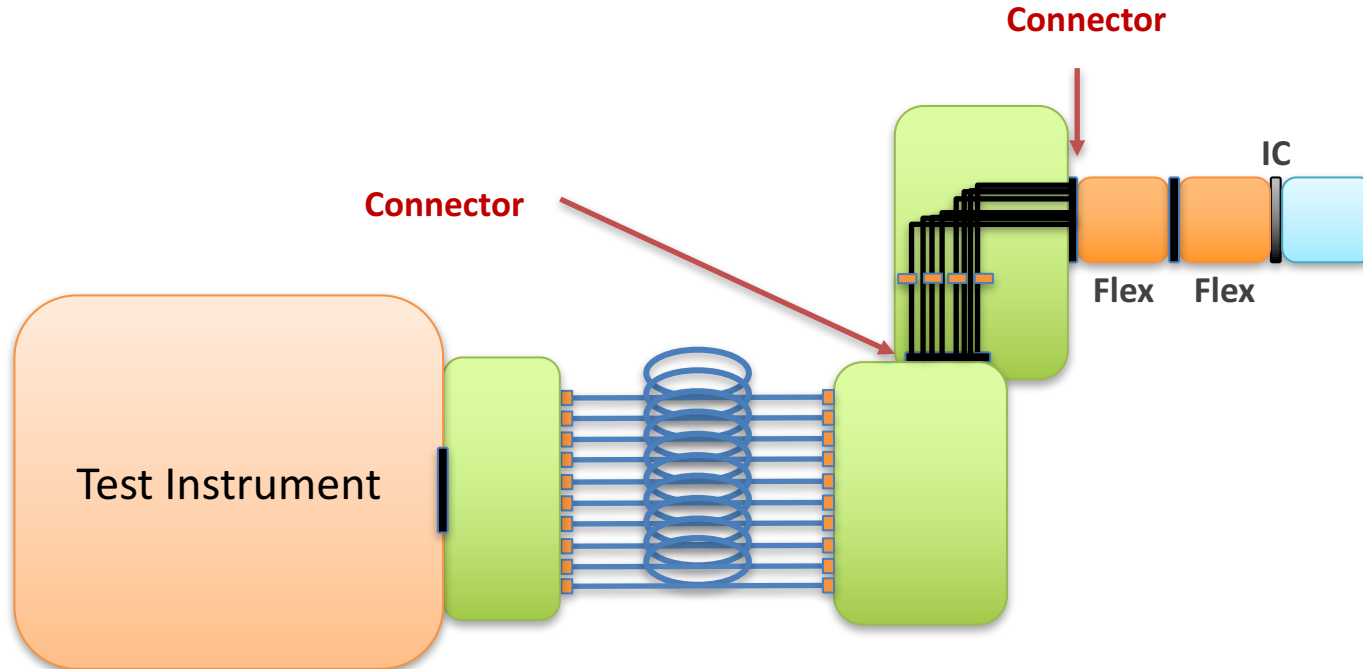
## Stub Causing Complete Eye Closure



## Slowing Data Rate Reveals Stub Waveform



# Practical Experiences: Grounding

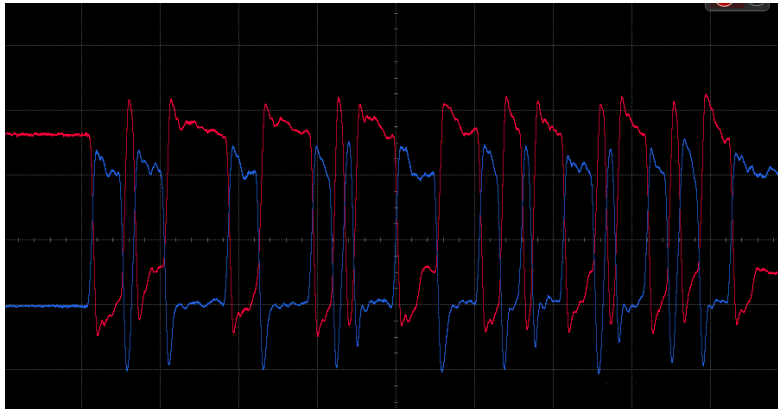




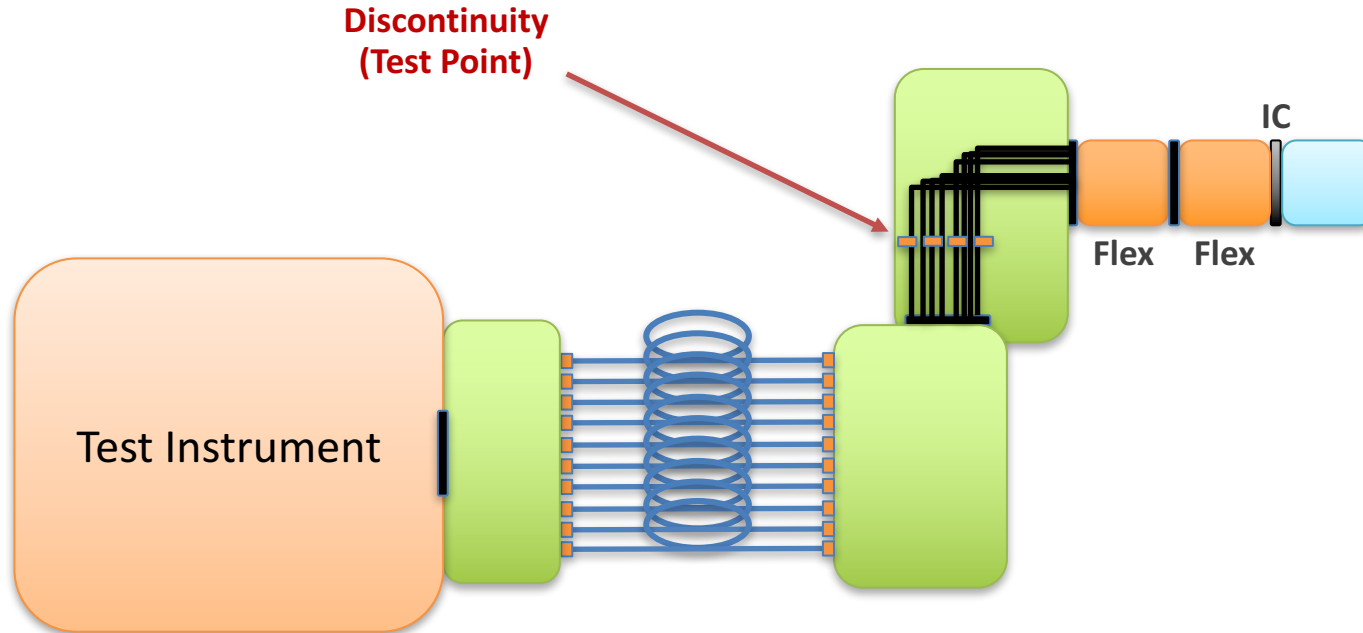
# Practical Experiences: Grounding

HS eye diagram affected by CM and Diff effects

LP waveforms affected by crossing levels and reflections



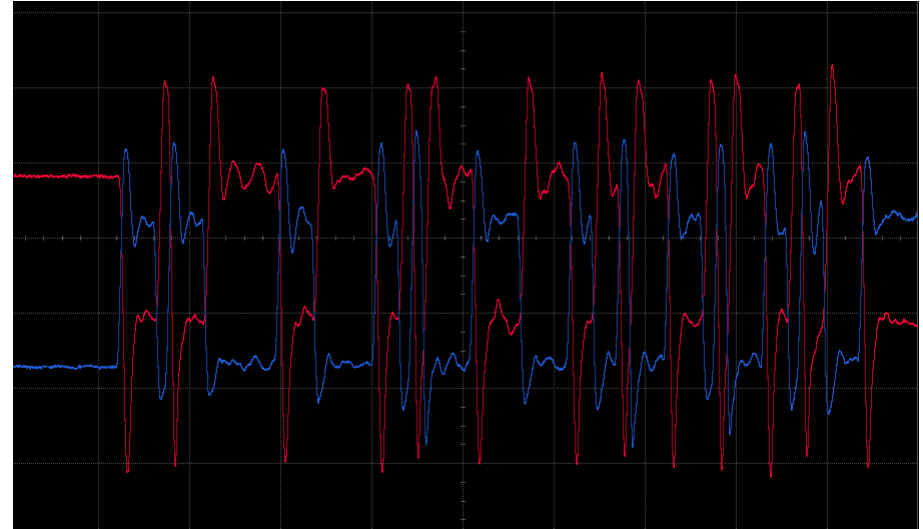
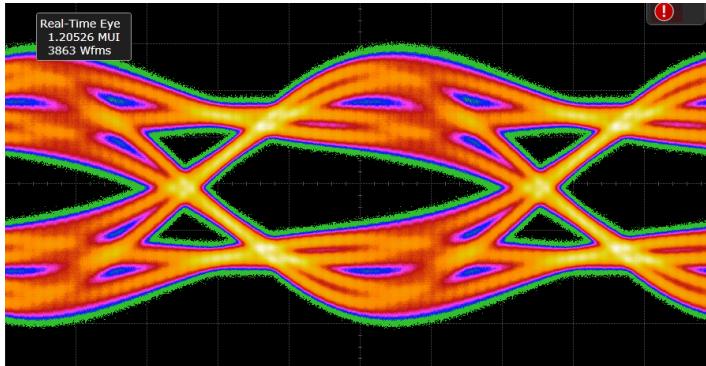
# Practical Experiences: Inductance



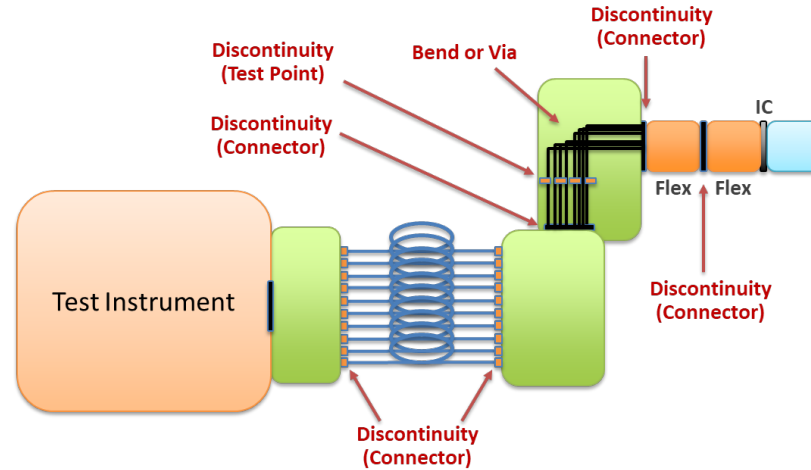
# Practical Experiences: Inductance

Often due to test points

Makes correlation with specifications difficult



# Recommendations



Adopt a **system-level approach to test** if your evaluation board looks like a system

Identify stress parameters **suitable for characterizing your device** in its environment

Characterize receiver performance **across system constraints** as well as through ideal “linear” channel



THANK YOU

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