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MIPI D-PHY\textsuperscript{SM} and MIPI CSI-2\textsuperscript{SM} for IoT: AI Edge Devices
Agenda

• Cloud vs. Edge Processing
  – History
  – Benefits and Tradeoffs
• Perceive Ergo® Edge Inference Processor Overview
  – Block diagram
  – target solutions
  – Target Applications
• Why MIPI?
• Why FDSOI?
• Mixel MIPI IP and Silicon Results
Back to the Future

• Processing was traditionally done on-site before moving to the cloud
• More recently there a transition to do more processing at the edge using AI
• Edge and cloud processing complement each other and necessary in many applications to achieve optimal system performance
• What is the most efficient system partitioning between cloud and edge?
Benefits of Processing at the Edge

• Latency: able to make decisions in real-time or near real-time vs. increased latency when processing at a data center or in the cloud
• Power savings: smarter devices require less bandwidth, provide less false notifications which can improve battery life
• Security and privacy: reduced chance for breach by reducing transmission of raw data to be processed somewhere else
• Connectivity: in some cases, connecting to broadband or even mobile may not be feasible so local computing is required
• Connection costs: even if connection is available, it may be worthwhile to save bandwidth due to connectivity costs
Benefits of Cloud Processing

• Performance: cloud processing offers massive computing capability that can’t be replicated at the edge, essential for complex machine learning and modeling

• Storage Capacity: offers large storage capacity

• Scalability: ability to expand storage and compute resources at incremental cost

• Security: cloud provides high security once the data is in the data center, but at-risk during data transmission

• Maintenance & Upgradeability: easier to maintain and upgrade
# Tradeoffs Between Edge vs. Cloud

<table>
<thead>
<tr>
<th>Features</th>
<th>Edge Computing</th>
<th>Cloud Computing</th>
</tr>
</thead>
<tbody>
<tr>
<td>Latency</td>
<td>Low</td>
<td>High</td>
</tr>
<tr>
<td>Bandwidth Required</td>
<td>Low</td>
<td>High</td>
</tr>
<tr>
<td>Processing Power</td>
<td>Low</td>
<td>High</td>
</tr>
<tr>
<td>Storage Capacity</td>
<td>Low</td>
<td>High</td>
</tr>
<tr>
<td>Security</td>
<td>High: no transmission</td>
<td>Medium: cloud backup</td>
</tr>
<tr>
<td>Cost</td>
<td>Medium</td>
<td>High</td>
</tr>
<tr>
<td>Scalability</td>
<td>High: # of devices</td>
<td>High: Processing/storage</td>
</tr>
<tr>
<td>Context Awareness</td>
<td>High</td>
<td>Low</td>
</tr>
<tr>
<td>Power Efficiency</td>
<td>High</td>
<td>Low</td>
</tr>
<tr>
<td>Maintenance &amp; Upgradeability</td>
<td>Medium</td>
<td>High</td>
</tr>
</tbody>
</table>

**Key**
- **Advantage**
- **Disadvantage**
Perceive ERGO™

- Edge inference processor for use in devices such as security cameras or smart appliances
  - 20-100x more power-efficient, delivering 4 sustained GPU-equivalent floating-point TOPS at 55 TOPS/W
  - Able to process large neural networks in 20mW and supports a variety of advanced neural networks
Overview of System Design

Sensors
- Audio
- Image
- Other

MIPI CSI-2 or MIPI CPI

I2C, MIPI I3C, etc.

Ergo
- Edge AI / ML Processor

Host System
- Device Processor (SoC)
- Storage
- Wi-Fi / BT Module
- SDRAM

I2S

Audio (Optional)

Perceptions

Video (Optional)

NOR Flash (Optional)
Ergo Block Diagram

- Built on GLOBALFOUNDRIES® 22FDX® Platform
- Integrates Mixel’s MIPI D-PHY CSI-2 TX/RX IP at 1.5Gbps/lane
Perceive Target Solutions

• Video Object Detection
  – Enables home or enterprise security to detect interesting motion and ignore false alerts

• Audio Event Detection
  – Able to detect critical sounds around the device to improve safety and contextual awareness

• Face Recognition
  – Can be used as standalone biometric or part of multi-factor authentication to unlock devices or objects

• Speech Recognition
  – Used for wakeup words, device-specific commands, and natural language interfaces for smartphone, smart toy, or home appliance
Perceive Target Applications

- Smart Home – Security Cameras and Doorbells
  - Detect interesting motion and ignore false alerts
  - Recognize faces, voices, and people
  - Detect relevant objects – animals, packages, vehicles, etc.
  - Use voice for local commands
  - Detect important sounds – alarms, people, glass breaking, etc.
  - Describe people, vehicles, or even the actions in a scene
Edge and Cloud in Smart Home Security System

- Ergo would reside at the edge and process data locally
- The edge devices would monitor movements, sounds, temperature, etc.
- Edge devices are typically connected to a local hub that is also connected to the cloud
- The cloud can be used for long term retention as well as machine learning
Perceive Target Applications

- Wearables
  - Detect important sounds around the user
  - Use local voice commands and advanced wake words to simplify device UI
  - Recognize faces, people, voice, and emotions
  - Detect relevant objects around the user
  - Integrate data across multiple sensors
Edge and Cloud in Wearable Applications

- Ergo would be embedded into the wearable device
- Through the built-in sensors, it would be monitoring the environment and identify objects, people, sounds, etc.
- The wearable would connect to the cloud and internet, through a mobile device for example, where a huge database is available including contacts, images, global maps, encyclopedias, etc.
- Like home security, the cloud can be used for long term retention, machine learning, and other applications
Perceive Target Applications

- Portable computing
  - Detect and recognize people and faces
  - Detect other relevant objects and sounds
  - Recognize voices and local voice commands
  - Track emotions, attention, and eye location
  - Blur or replace video conference backgrounds
  - Improve audio or video signal
Perceive Target Applications

- Video conferencing
  - Detect and track people, faces, and voices
  - Recognize individual faces and voices
  - Audio noise reduction and intelligent muting
  - Use gesture or voice for touchless control
  - Blur or replace video conference backgrounds
  - Gaze correction and audience analytics
  - Detect other relevant objects and sounds
Why MIPI

• MIPI was designed from the ground up to minimize power requirements while supporting high bandwidth and strict EMI requirements
• Many edge applications are battery operated
• MIPI CSI-2 is widely adopted for sensor applications
• MIPI D-PHY is the first and most widely adopted MIPI PHY today

Typical IoT Device with cellular connectivity using many MIPI specifications
Why FDSOI

- FDSOI provides the right mix to achieve better performance, with lower power, at lower cost—without the need to move to more costly FinFET processes
- Compared with bulk silicon, FDSOI provides additional flexibility, due to the programmability of body bias, resulting in higher performance and potential reduction in power and area
  1. Body biasing allows trade-off between dynamic and leakage power resulting in lowest possible power consumption for workload and operating conditions
  2. Reverse Body Bias (RBB) can be applied during stand-by mode to drastically reduce leakage current
  3. FDSOI enables performance/frequency boost through Forward Body Bias (FBB)
- No wonder FDSOI is widely adopted for IoT devices!
Power and Area Saving Evaluation

• By applying Forward Body Bias (FBB), device size was minimized while maintaining the same performance.
• By adjusting the FBB and RBB based on PVT you can either reduce the area or the power or both.
• Achievable power and area savings for FDSOI process:
  • Active area reduction of ~55%
  • Power reduction of 14-50% across PVT.
Mixel MIPI Receiver IP

- MIPI D-PHY CSI-2 RX IP
- Supports MIPI D-PHY v2.1 with backwards compatibility for v1.2 and v1.1
- High-speed transmitter running at 2.5Gbps/lane
- Low-power transmitter running at up to 80Mbps/lane
- 2 and 4 data lanes and 1 clock lane configurations
- Area optimized
- Achieved first time silicon success
Mixel MIPI Transmitter IP

- MIPI D-PHY CSI-2 TX IP
- Supports MIPI D-PHY v2.1 with backwards compatibility for v1.2 and v1.1
- High-speed transmitter running at 2.5Gbps/lane
- Low-power transmitter running at up to 80Mbps/lane
- 4 data lanes and 1 clock lane
- Area optimized
- Achieved first time silicon success
Silicon Results

MIPI D-PHY TX @ 1.5Gbps

MIPI D-PHY TX @ 2.5Gbps
## Mixel IP in FDSOI Processes

<table>
<thead>
<tr>
<th>IP Name</th>
<th>Features</th>
<th>Node</th>
</tr>
</thead>
<tbody>
<tr>
<td>D-PHY Universal</td>
<td>V1.2; 2.5Gbps; De-skew; loopback testability.</td>
<td>Silicon-Proven in 28FDSOI</td>
</tr>
<tr>
<td>D-PHY Universal</td>
<td>800Mbps; Ultra low power; Wearables, IoT.</td>
<td></td>
</tr>
<tr>
<td>D-PHY DSI TX</td>
<td>1.5Gbs; low Skew; Test modes.</td>
<td></td>
</tr>
<tr>
<td>D-PHY CSI-2 RX</td>
<td>1.5Gbs; Test modes.</td>
<td></td>
</tr>
<tr>
<td>LVDS TX</td>
<td>1.25Gbps; 4 Channel; 7 or 10 bits/channel</td>
<td></td>
</tr>
<tr>
<td>LVDS TX</td>
<td>1.25Gbps; 8 Channel; 7 or 10 bits/channel</td>
<td></td>
</tr>
<tr>
<td>LVDS/D-PHY TX Combo</td>
<td>1.05Gbps; 4 Channel; Test modes.</td>
<td></td>
</tr>
<tr>
<td>LVDS/D-PHY TX Combo</td>
<td>1.05Gbps; 8 Channel; Test modes.</td>
<td></td>
</tr>
<tr>
<td>D-PHY CSI-2 TX</td>
<td>2.5 Gbps/lane; 4 lanes</td>
<td>Silicon-Proven in 22FDX</td>
</tr>
<tr>
<td>D-PHY CSI-2 RX</td>
<td>2.5Gbps/lane; 2 or 4 lanes</td>
<td></td>
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<tr>
<td>D-PHY CSI-2 RX</td>
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</table>
Mixel MIPI PHY Portfolio

- Industry leader in MIPI® interfaces and contributing member of the MIPI Alliance since 2006
  - MIPI D-PHY first silicon-proven in 2008
  - MIPI M-PHY® first silicon-proven in 2011
  - MIPI C-PHY first silicon-proven in 2016

- Complete integrated solution includes PHY, controller, and platform

- Widest coverage of process nodes and foundries: silicon-proven in 11 different nodes and 8 different foundries
Conclusion

• Connected systems must strike balance between cloud and edge processing to optimize system performance
• MIPI specifications are uniquely designed to enable low power, high bandwidth requirements of edge devices
• FDSOI provides high performance with lower power at lower cost
• Processors like Perceive Ergo enable AI processing at the edge to make connected devices smarter, resulting in lower latency, improved battery life, and better security
• Mixel MIPI PHY IP enables SoC designers to leverage the benefits of MIPI with silicon-proven designs in FDSOI, lowering project risk
THANK YOU!
REFERENCES & ADDITIONAL RESOURCES

- From Cloud to Edge
- A look at examples of IoT devices and their business applications in 2021
- What is edge computing? Everything you need to know
- Edge Intelligence Makes Smart Homes Truly Intelligent
- Autonomous Vehicles Drive AI Advances for Edge Computing
- Smart manufacturing and the IoT are driving the Industry 4.0 revolution
- Smart Manufacturing: Cloud vs. Edge Computing
- MIPI White Paper: Enabling the IoT Opportunity
- It’s Time to Look at FDSOI Again