MIPI I3C Under the Spotlight: A Fireside Chat with the I3C Experts
New Specifications for 2021

• **I3C Version 1.1.1**
  – Clarifications (Small errors and typos)
  – Terminology changes (Controller/Target)
  – Incorporation of previous modifications (Target Reset)

• **I3C Basic Version 1.1.1**
  – Synchronized with I3C v1.1.1
  – Clarifications and Terminology changes
  – New features (HDR Modes, CCCs, Timing Control)

• **I3C Host Controller Interface (HCI) Version 1.1**
  – New features (Grouped Addressing, Dynamic Address Assignment, CCCs with Defining Bytes, Target Reset etc.)
  – Synchronized with I3C v1.1.1 / I3C Basic v1.1.1
  – Clarifications and Terminology changes
Where is I3C Being Leveraged?

- **JEDEC**
  - Serial Presence Detect (SPD) Hub for DDR Memory
- **DMTF**
  - Management Component Transport Protocol (MCTP) and I3C protocols for bus management
- **Others in development**
  - PCIe I3C sideband
  - I3C over IEEE 1722
  - Protocol Adaptation Layer (PAL) for I3C over A-PHY
  - ETSI for Smart Secure Platform
  - More?
Get Involved / Sources of Further Information

• **I3C Working Group**
  – Open to MIPI contributor members (Meets: Wednesdays 8am PST)

• **Contact the Working Group**
  – Let us know your questions/comments
  – Email: i3c@mipi.org (members)
  – Email: admin@mipi.org (non-members)

• **I3C Supporting Documents**
  – FAQs
  – App Notes
  – Errata

• **Website**: [https://www.mipi.org/specifications/i3c-sensor-specification](https://www.mipi.org/specifications/i3c-sensor-specification)